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Application Serial No	09/653,156
Filing Date	
nventor	Vishnu K. Agarwal
Assignee	licron Technology, Inc.
Group Art Unit	
Examiner	
Attorney's Docket No.	

Title: Enhanced Surface Area Capacitor Fabrication Methods

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

Assistant Commissioner for Patents Attn: Art Unit 2831 Washington, D.C. 20231

<u>REMARKS</u>

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449 and copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of these patents is respectfully requested.

Respectfully submitted,

Date:

18 Feb Dos

By:

James JE. (L'ake Reg. No. 44,854

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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Coplication Serial No	09/653,156
Filing Date	ugust 31, 2000
Inventor Vish	ınu K. Agarwal
Assignee Micron Te	echnology, Inc.
Group Art Unit	2831
Examiner	
Attorney's Docket No	MI22-1518
Title: Capacitor Fabrication Methods and Capacitor Constructions	

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

Assistant Commissioner for Patents Washington, D.C. 20231

REMARKS

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449 and copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of these patents is respectfully requested.

This Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing of a first Office Action, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this information disclosure statement, please charge the fee specified under 37 C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Respectfully submitted,

By: Date: Jamés JE.

Reg. No. 44,854